



X-923 US
10/092,051

PATENT
Conf. No.: 7802

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Douglass
Assignee: Xilinx, Inc.
Title: Method of Designing Integrated Circuit Having
Both Configurable and Fixed Logic Circuitry

Serial No.:	10/092,051	File Date:	03-04-02
Examiner:	Stacy Whitmore	Art Unit:	2812
Docket No.:	X-923 US	Conf. No.:	7802

Mail Stop NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on January 12, 2004, please replace/substitute the following paragraph(s)/section(s)/claim(s) as indicated.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.